



PATENT

DPW

IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

APPLICANT(S): Harish R. Devanagondi, et al.  
APPLICATION NO.: 10,612,889  
FILING DATE: July 3, 2003  
TITLE: Multi-Slice Network Processor  
EXAMINER: not yet known  
GROUP ART UNIT: 2661  
ATTY. DKT. NO.: 23019-07337

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

Dated: 7/6/05 By: Greg T. Sueoka  
Greg T. Sueoka, Reg. No.: 33,800

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**STATUS REQUEST**

SIR:

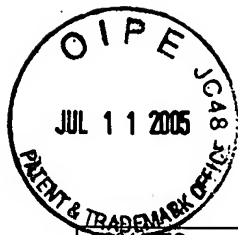
Our file for the subject application reveals that there has been no action on this application since the filing of Information Disclosure Statement on December 3, 2003.

Please inform the undersigned, at the below stated address, of the status of this application.

Respectfully submitted,  
HARISH R. DEVANAGONDI, ET AL.

Dated: 7/6/05

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PTO/SB/21 (modified)

Approved for use through xx/xx/xx, OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

0001 PTO Rev. 10/95		U.S. Department of Commerce Patent and Trademark Office		Application Number	10/612,889		
<b>TRANSMITTAL FORM</b>  (to be used for all correspondence during pendency of filed application)				Filing Date	July 3, 2003		
				First Named Inventor	Harish R. Devanagondi		
				Group Art Unit Number	2661		
				Examiner Name	not yet known		
Total Number of Pages in This Submission		3	Attorney Docket Number		23019-07337		

**ENCLOSURES (check all that apply)**

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REMARKS:

**SIGNATURE OF ATTORNEY OR AGENT**

Signature:		
Attorney/Reg. No.:	Greg T. Sueoka/Reg. No. 33,800	Dated: 7/6/05

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